



1N746 thru 1N759A, -1 and
1N4370 thru 1N4372A, -1 DO-35

Silicon 500 mW Zener Diodes

ELECTRICAL CHARACTERISTICS* @ 25°C

JEDEC TYPE NO. (NOTE 1)	NOMINAL ZENER VOLTAGE $V_Z @ I_{ZT}$ (NOTE 2) VOLTS	ZENER TEST CURRENT I_{ZT} mA	MAXIMUM ZENER IMPEDANCE $Z_{ZT} @ I_{ZT}$ (NOTE 3) OHMS	MAXIMUM REVERSE CURRENT I_R @ $V_R = 1$ VOLT		MAXIMUM ZENER CURRENT I_{ZM} (NOTE 4) mA	TYPICAL TEMP COEFF. OF ZENER VOLTAGE α_{VZ} %/°C
				@25°C	@+150°C		
				μA	μA		
1N4370	2.4	20	30	100	200	150	-.085
1N4371	2.7	20	30	75	150	135	-.080
1N4372	3.0	20	29	50	100	120	-.075
1N746	3.3	20	28	10	30	110	-.066
1N747	3.6	20	24	10	30	100	-.058
1N748	3.9	20	23	10	30	95	-.046
1N749	4.3	20	22	2	30	85	-.033
1N750	4.7	20	19	2	30	75	-.015
1N751	5.1	20	17	1	20	70	+/- .010
1N752	5.6	20	11	1	20	65	+ .030
1N753	6.2	20	7	.1	20	60	+ .049
1N754	6.8	20	5	.1	20	55	+ .053
1N755	7.5	20	6	.1	20	50	+ .057
1N756	8.2	20	8	.1	20	45	+ .060
1N757	9.1	20	10	.1	20	40	+ .061
1N758	10.0	20	17	.1	20	35	+ .062
1N759	12.0	20	30	.1	20	30	+ .062

* JEDEC Registered Data

NOTE 1: Standard tolerance on JEDEC types shown is +/- 10%. Suffix letter A denotes +/- 5% tolerance; suffix letter C denotes +/- 2%; and suffix letter D denotes +/- 1% tolerance.

NOTE 2: Voltage measurements to be performed 20 seconds after application of dc test current.

NOTE 3: Zener impedance derived by superimposing on I_{ZT} , a 60 cps, rms ac current equal to 10% I_{ZT} (2mA ac). See MicroNote 202 for typical zener impedance variation with different operating currents.

NOTE 4: Allowance has been made for the increase in V_Z due to Z_Z and for the increase in junction temperature as the unit approaches thermal equilibrium at the power dissipation of 400 mW.